



PATENT
Attorney Docket No. QCS-001DV3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Kamieniecki et al. CONFIRMATION NO.: 5384
SERIAL NO.: 09/932,754 ART UNIT: 2829
FILING DATE: August 17, 2001 EXAMINER: J. Hollington
TITLE: Real-Time In-Line Testing of Semiconductor Wafers

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE

Sir:

In response to the Office Action mailed on July 13, 2004, in connection with the patent application identified above, for which the period of response ends on January 13, 2005, Applicants respectfully request entry of this response, reconsideration, and passage of this application to allowance.

Applicants enclose a request for a two month extension of time, up to and including December 13, 2004, and two checks totaling the amount of \$225.00. Applicants believe that no further fees are due upon submission of this paper. However, if any fees are due, the Commissioner is hereby authorized to charge them to Deposit Account No. 20-0531.

Amendments to the Claims are reflected in the listing of claims that begin on page 2 of this paper.

The **Response** begins on page 4 of this paper.

The **Conclusion** begins on page 7 of this paper.